

RKR0303AKJ

Silicon Schottky Barrier Diode for Rectifying

R07DS0249EJ0300 Rev.3.00 Jan 25, 2011

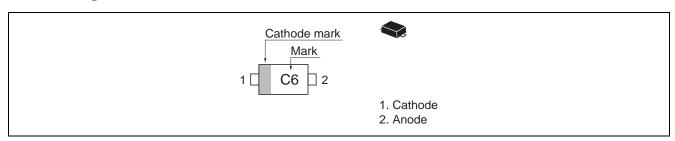
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- Ultra small Flat Lead Package (UFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKR0303AKJ P	C6	UFP	PWSF0002ZA-A	P (4,000pcs / reel)
RKR0303AKJ R	Co			R (8,000pcs / reel)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	30	V
Reverse voltage	V_R	30	V
Forward current	I _F * ¹	0.3	A
Non-Repetitive peak forward surge current	I _{FSM} * ²	1	Α
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. See from Fig.4 to Fig.6.

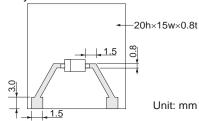
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _F	_	_	0.42	V	I _F = 300 mA
Reverse current	I _R	_	_	200	μΑ	V _R = 30 V
Thermal resistance	Rth(j-a)	_	600	_	°C/W	Polyimide board *1

Notes: 1. Polyimide board



2. In the UFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristics

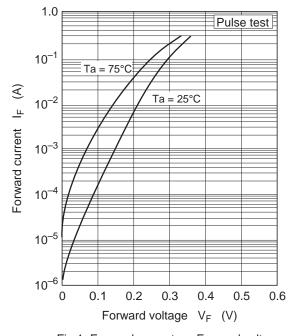


Fig.1 Forward current vs. Forward voltage

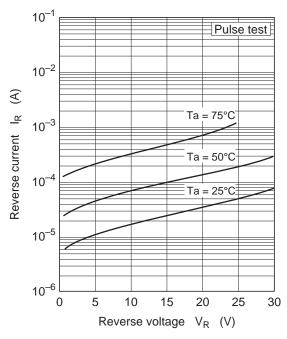
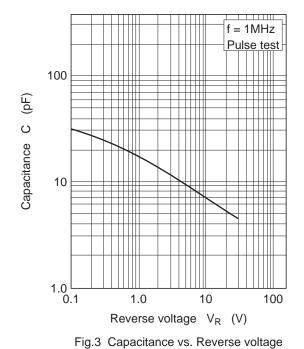
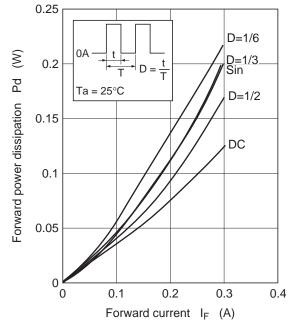


Fig.2 Reverse current vs. Reverse voltage







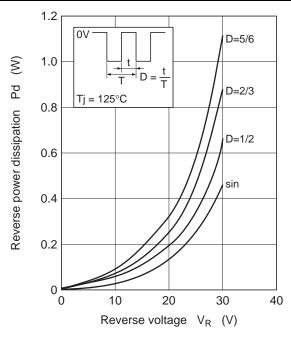


Fig.5 Reverse power dissipation vs. Reverse voltage

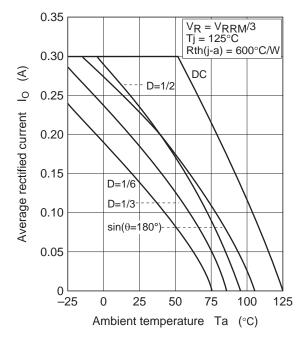
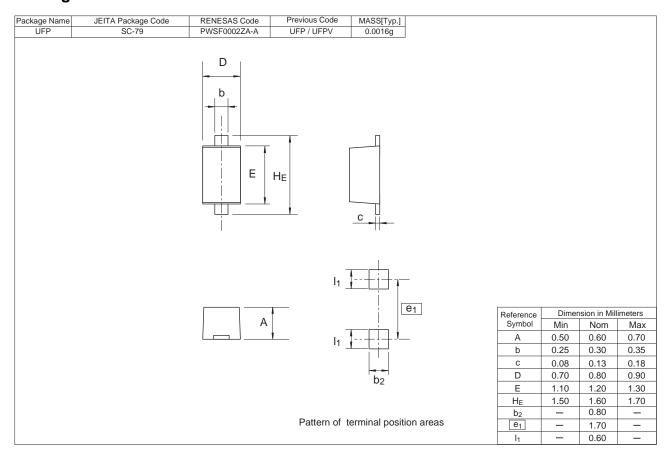


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions



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